

REMARKS

Amendment to Claims

In order to advance the prosecution of this application, Applicants have amended independent Claims 1-8 to recite that each step of a voltage level for said voltage gray scale method is designated as $(VH-VL)/2^n$, where VH is the highest voltage level of voltages inputted to a D/A converter circuit, and VL is the lowest voltage level of voltages inputted to said D/A converter circuit. An example of this feature is shown in the present application, in Fig. 5, and the first two paragraphs on page 12 of the specification, where $m = 5$ and $n = 2$.

Applicants have the following response to the Office Action of January 28, 2004. Applicants will address each of the Examiner's rejections in the order in which they appear in the Office Action.

Claim Rejections - 35 USC §103

Claims 1, 5, 10, 11, 29 and 36

In the Office Action, the Examiner rejects Claims 1, 5, 10, 11, 29 and 36 under 35 USC §103(a) as being unpatentable over Okada et al. in view of Yamaguchi et al. This rejection is respectfully traversed.

Since neither Okada nor Yamaguchi disclose or suggest a D/A converter, as recited in the claims of the present application, these claims are patentable over the cited references. Therefore, it is respectfully requested that this rejection be withdrawn.

Claims 2, 6, 26, 30, 33 and 37

The Examiner further rejects Claims 2, 6, 26, 30, 33 and 37 under 35 USC §103(a) as

being unpatentable over Okada et al. in view of Yamaguchi et al. and further in view of Yasunishi. This rejection is also respectfully traversed.

Since neither Okada, Yamaguchi, nor Yasunishi disclose or suggest a D/A converter, as recited in the claims of the present application, these claims are patentable over the cited references. Therefore, it is respectfully requested that this rejection be withdrawn.

Claims 3, 4, 8, 27, 28, 32, 34, 35 and 39

The Examiner further rejects Claims 3, 4, 8, 27, 28, 32, 34, 35 and 39 under 35 USC §103(a) as being unpatentable over Okada et al. Yamaguchi et al, Yasunishi and further in view of Yamazaki et al. This rejection is also respectfully traversed.

As explained above, neither Okada, Yamaguchi, nor Yasunishi disclose or suggest a D/A converter circuit. Further, while Yamazaki discloses a D/A conversion circuit, neither Yamazaki nor any of these other cited references disclose or suggest that each step of a voltage level for said voltage gray scale method is designated as $(VH-VL)/2^n$, where VH is the highest voltage level of voltages inputted to a D/A converter circuit, and VL is the lowest voltage level of voltages inputted to said D/A converter circuit, as recited in the claims of the present application.

Therefore, the claims are patentable over these cited references, and it is respectfully requested that this rejection be withdrawn

Claims 7, 31 and 38

The Examiner also rejects Claims 7, 31 and 38 under 35 USC §103(a) as being unpatentable over Okada et al. in view of Yamaguchi et al and further in view of Yamazaki et al. This rejection is also respectfully traversed.

For at least the reasons discussed above, each of these claims is also patentable over the cited references, and it is requested that this rejection be withdrawn.

Claims 12-17, 40-74, 76-82 and 89-152

The Examiner further rejects Claims 12-17, 40-74, 76-82 and 89-152 under 35 USC §103(a) as being unpatentable over Yamazaki et al. or Holmes et al, or Kimura, or Munyan, or Stambolic et al., or Kleinschmidt et al., or Sato, or Yun et al. in view of Okada et al and Yamaguchi et al or Okada et al. Yamaguchi et al., and Yasunishi or Okada et al. Yamaguchi et al. Yasunishi and Yamazaki or of Okada et al., Yamaguchi, and Yamazaki. This rejection is also respectfully traversed.

Each of these claims is a dependent claim. Accordingly, for at least the reasons discussed above for the independent claims, these claims are also patentable over the cited references, and it is requested that this rejection be withdrawn.

Claims 9 and 19-25

The Examiner further rejects Claims 9 and 19-25 under 35 USC §103(a) are rejected as being unpatentable over Okada et al., and Yamaguchi et al, or of Okada et al., Yamaguchi et al., and Yasunishi, or of Okada et al., Yamaguchi et al., Yasunishi, and Yamazaki, or of Okada et al., Yamaguchi et al., and Yamazaki and further in view of Wu et al. This rejection is also respectfully traversed.

Each of these claims is a dependent claim. Accordingly, for at least the reasons discussed above for the independent claims, these claims are also patentable over the cited references, and it is requested that this rejection be withdrawn.

Claims 18, 83-88 and 153

Claims 18, 83-88 and 153 under 35 USC §103(a) are rejected as being unpatentable over Okada et al., Yamaguchi et al, or of Okada et al., Yamaguchi et al., and Yasunishi or of Okada et al., Yamaguchi et al., Yasunishi, and Yamazaki or of Okada et al. Yamaguchi et al., and

Yamazaki and further in view of Bhargava. This rejection is also respectfully traversed.

Each of these claims is a dependent claim. Accordingly, for at least the reasons discussed above for the independent claims, these claims are also patentable over the cited references, and it is requested that this rejection be withdrawn.

Therefore, for at least the above-stated reasons, it is respectfully submitted that all of the §103 rejections has been overcome.

Conclusion

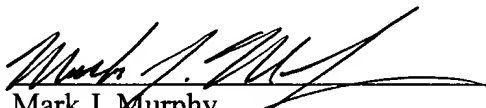
It is respectfully submitted that the present application is now in a condition for allowance and should be allowed.

Please charge our deposit account 50/1039 for any further fee for this amendment.

Favorable reconsideration is earnestly solicited.

Respectfully submitted,

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Mark J. Murphy
Registration No.: 34,225
COOK, ALEX, McFARRON, MANZO,
CUMMINGS & MEHLER, LTD.
200 West Adams Street, Suite 2850
Chicago, Illinois 60606
(312) 236-8500